

FIG.2 101 CONTROLLER 111 102 **OPERATION PART CPU** 113 108 103 104 106 2nd PCI 109 **ASIC** MEM-P NB **AGP ENGINE** 105 MEM-C SB 112 110 107



FIG.3



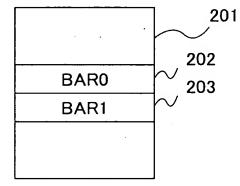
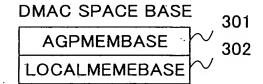
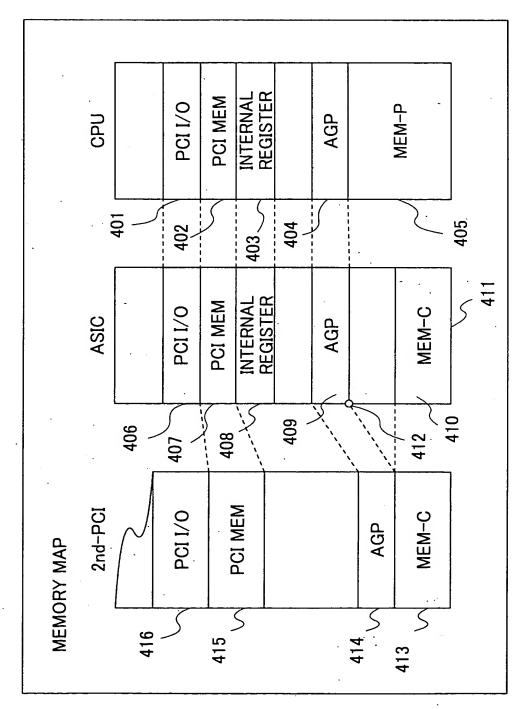


FIG.4



**FIG.5** 



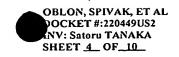
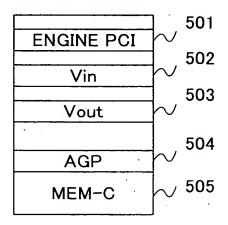
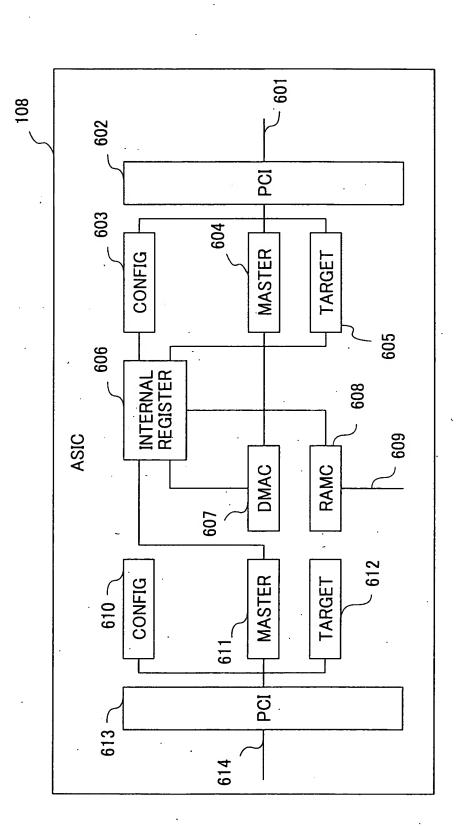


FIG.6

## 2nd-PCI SPACE





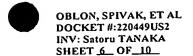


FIG.8

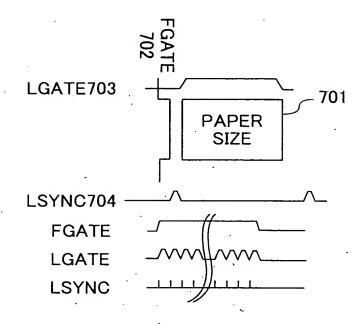


FIG.9

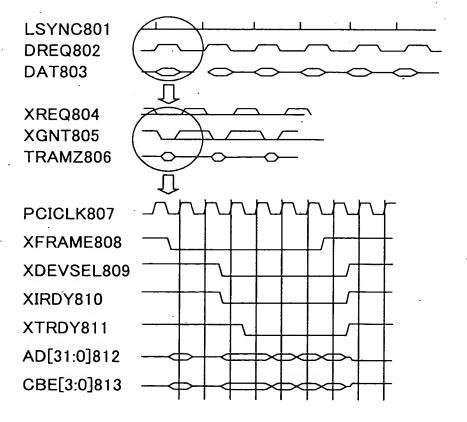




FIG.10

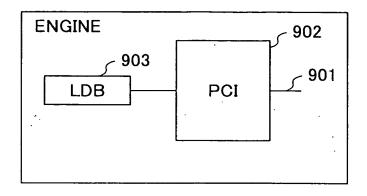
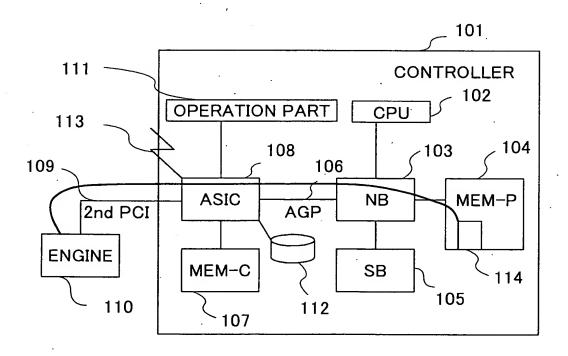


FIG.11



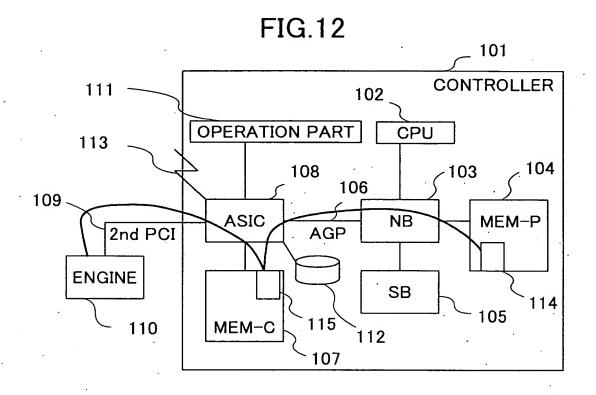
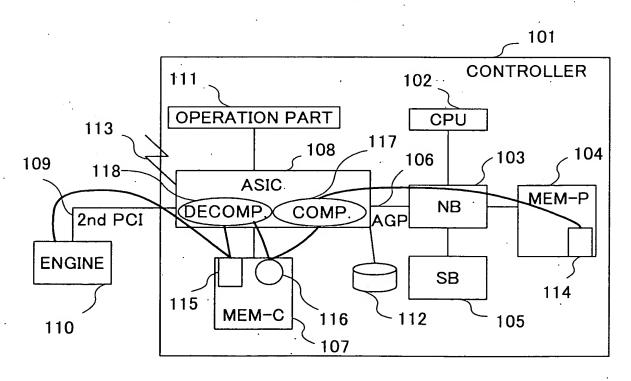


FIG.13





**FIG.14** 101 CONTROLLER 111 102 **OPERATION PART** CPU 113 104 103 108 106 109 **ASIC** MEM-P NB DECOMP 2nd PCI 118 **ENGINE** SB 119 114 112 MEM-C 105 110 107

FIG.15

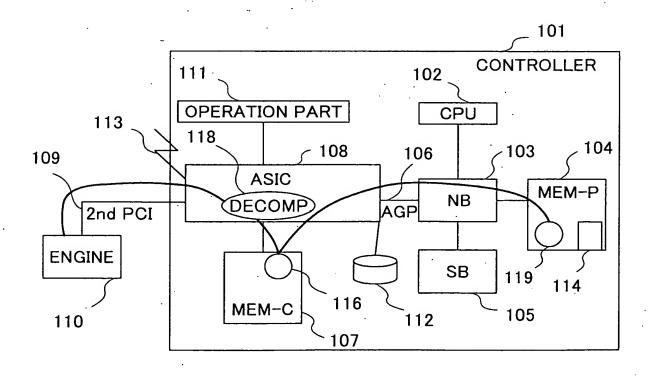


FIG.16

